JNITED STATES PATENT AND TRADE

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rd Forbes et al.

RCUITS WITH A TRENCH CAPACITOR HAVING MICRO-ROUGHENED SEMICONDUCTOR **SURFACES**

Docket No.:

303.389US2

Serial No.: 09/467.992

Filed:

December 20, 1999

Due Date: March 22, 2001

Examiner:

Eugene Lee

Group Art Unit: 2815

Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.

- An Amendment and Response (10 Pages).
- A Supplemental Information Disclosure Statement (1 pgs.), Form 1449 (1 pgs.), and copies of 5 cited references.
- A check in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97
- A check in the amount of \$72.00 to cover the fee for additional claims as calculated below.
- Clean Version of Pending Claims (5 pgs.).
- Marked-Up Version of the Amended Paragraph (1 pg.).
- Copy of US Patent No. 6,025,225.

If an additional fee is required due to changes to the claims, the fee has been calculated as follows:

CLAIMS AS AMENDED						
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Present Extra	Rate	Fee
TOTAL CLAIMS	24	-	20	4	x 18 =	\$72.00
INDEPENDENT CLAIMS	7		7		x 10 =	\$0.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED						\$0.00
TOTAL						\$72.00

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 22 day of March, 2001.

Name

Signature

Customer Number 21186

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(GENERAL)

MAR 2 6 2001 S/N 19/467,992 IN

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes et al.

Serial No .:

09/467,992

Filed:

December 20, 1999

Title:

CIRCUITS WITH A TRENCH CAPACITOR HAVING MICRO-ROUGHENED

PATENT

Examiner: Eugene Lee

Group Art Unit: 2815

Docket: 303.389US2

SEMICONDUCTOR SURFACES

AMENDMENT AND RESPONSE

Commissioner for Patents Washington, D.C. 20231

Applicant has carefully reviewed the Office Action mailed December 22, 2000. Detailed comments to the Office Action are found in the Remarks section below. Amendments to the specification and claims are found in the following Amendments section.

IN THE SPECIFICATION

Please replace the following paragraph with the paragraph, as amended, below. Applicant has enclosed a marked-up version of the below-amended paragraph in the appendix entitled "Marked-Up Version of the Amended Paragraph".

On page 6, line 20, please replace the paragraph with the following:

Word line 112 passes body region 108 of access transistor 111 in isolation trench 114. Word line 112 is separated from body region 108 of access transistor 111 by gate oxide 116 such that the portion of word line 112 adjacent to body region 108 operates as a gate for access transistor 111. Word line 112 may comprise, for example, N+ poly-silicon material that is deposited in isolation trench 114. Cell 102D is coupled in a column with cell 102A by bit line 118.

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